Amendments to the Specification:

Please replace the paragraph beginning on line 11, page 2 with the following paragraph:

The calculation circuit also may include a second data selector having a first and a second datum input connected to the output of the first data selector and, respectively, to the first output of the subtracter and receiving the first and the second output datum, respectively; a selection input; and an output. The selection input receives a third selection signal assuming a first level for the selection of an operating mode in fuzzy logic and a second level for the selection of an operating mode in non-fuzzy logic. The output supplies a ~~fourth~~ third output datum correlated to one of the first and second output datum as a function of the level of the third selection signal.